

RELIABILITY REPORT
FOR
MAX690xxxA
PLASTIC ENCAPSULATED DEVICES

July 25, 2002

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.

SUNNYVALE, CA 94086

Written by



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Conclusion

The MAX690 successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX690 microprocessor (μ P) supervisory circuit reduces the complexity and number of components required for power-supply monitoring and battery-control functions in μ P systems. It significantly improves system reliability and accuracy compared to separate ICs or discrete components.

The MAX690 is designed for use in systems powered by 3.0V or 3.3V supplies. The reset threshold voltages for this device is 3.075V. Some functions offered by this device include: active-low reset, watchdog input, and battery-backup power switching.

B. Absolute Maximum Ratings

<u>Item</u>	<u>Rating</u>
Terminal Voltage (with respect to GND)	
V_{CC}	-0.3V to 6.0V
VBATT	-0.3V to 6.0V
All Other Inputs	-0.3V to the higher of V_{CC} or VBATT
Continuous Input Current	
V_{CC}	100mA
VBATT	18mA
GND	18mA
Output Current	
/RESET, /PFO	18mA
V_{OUT}	100mA
Storage Temp.	-65°C to +160°C
Lead Temp. (10 sec.)	+300°C
Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)	
8-Pin PDIP	727mW
8-Pin SO	471mW
Derates above +70°C	
8-Pin PDIP	9.09mW/°C
8-Pin SO	5.88mW/°C

II. Manufacturing Information

A. Description/Function:	3.0V/3.3V Microprocessor Supervisory Circuit
B. Process:	S3 (Standard 3 micron silicon gate CMOS)
C. Number of Device Transistors:	802
D. Fabrication Location:	California or Oregon, USA
E. Assembly Location:	Philippines, Malaysia, or Korea
F. Date of Initial Production:	March, 1994

III. Packaging Information

A. Package Type:	8-Lead SO	8-Lead PDIP
B. Lead Frame:	Copper	Copper
C. Lead Finish:	Solder Plate	Solder Plate
D. Die Attach:	Silver-filled Epoxy	Silver-filled Epoxy
E. Bondwire:	Gold (1.3 mil dia.)	Gold (1.3 mil dia.)
F. Mold Material:	Epoxy with silica filler	Epoxy with silica filler
G. Assembly Diagram:	# 05-1701-0241	# 05-1701-0240
H. Flammability Rating:	Class UL94-V0	Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard JESD22-112:	Level 1	Level 1

IV. Die Information

A. Dimensions:	80 x 110 mils
B. Passivation:	Si ₃ N ₄ /SiO ₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect:	Aluminum/Si (Si = 1%)
D. Backside Metallization:	None
E. Minimum Metal Width:	3 microns (as drawn)
F. Minimum Metal Spacing:	3 microns (as drawn)
G. Bondpad Dimensions:	5 mil. Sq.
H. Isolation Dielectric:	SiO ₂
I. Die Separation Method:	Wafer Saw

V. Quality Assurance Information

- A. Quality Assurance Contacts: Jim Pedicord (Manager-Rel Operations)
Bryan Preeshl (Executive Director of QA)
Kenneth Huening (Vice President)
- B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.
0.1% For all Visual Defects.
- C. Observed Outgoing Defect Rate: < 50 ppm
- D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in **Table 1**. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{6.21}{192 \times 4389 \times 1430 \times 2} \quad (\text{Chi square value for MTTF upper limit})$$

↓
Temperature Acceleration factor assuming an activation energy of 0.8eV

$$\lambda = 2.58 \times 10^{-9}$$

$$\lambda = 2.58 \text{ F.I.T. (60\% confidence level @ 25°C)}$$

This low failure rate represents data collected from Maxim's reliability monitor program. In addition to routine production Burn-In, Maxim pulls a sample from every fabrication process three times per week and subjects it to an extended Burn-In prior to shipment to ensure its reliability. The reliability control level for each lot to be shipped as standard product is 59 F.I.T. at a 60% confidence level, which equates to 3 failures in an 80 piece sample. Maxim performs failure analysis on any lot that exceeds this reliability control level. Attached Burn-In Schematic (Spec. # 06-0526) shows the static Burn-In circuit. Maxim also performs quarterly 1000 hour life test monitors. This data is published in the Product Reliability Report (**RR-1M**).

B. Moisture Resistance Tests

Maxim pulls pressure pot samples from every assembly process three times per week. Each lot sample must meet an LTPD = 20 or less before shipment as standard product. Additionally, the industry standard 85°C/85%RH testing is done per generic device/package family once a quarter.

C. E.S.D. and Latch-Up Testing

The PW34-1 die type has been found to have all pins able to withstand a transient pulse of $\pm 800\text{V}$, per Mil-Std-883 Method 3015 (reference attached ESD Test Circuit). Latch-Up testing has shown that this device withstands a current of $\pm 250\text{mA}$ and/or $\pm 20\text{V}$.

Reliability Evaluation Test Results

MAX690xxxA

TEST ITEM	TEST CONDITION	FAILURE IDENTIFICATION	PACKAGE	SAMPLE SIZE	NUMBER OF FAILURES
Static Life Test (Note 1)					
	Ta = 135°C Biased Time = 192 hrs.	DC Parameters & functionality		1430	2
Moisture Testing (Note 2)					
Pressure Pot	Ta = 121°C P = 15 psi. RH= 100% Time = 168hrs.	DC Parameters & functionality	SO	77	0
			PDIP	77	0
85/85	Ta = 85°C RH = 85% Biased Time = 1000hrs.	DC Parameters & functionality		77	0
Mechanical Stress (Note 2)					
Temperature Cycle	-65°C/150°C 1000 Cycles Method 1010	DC Parameters		77	0

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data

Attachment #1

TABLE II. Pin combination to be tested. 1/ 2/

	Terminal A (Each pin individually connected to terminal A with the other floating)	Terminal B (The common combination of all like-named pins connected to terminal B)
1.	All pins except V_{PS1} <u>3/</u>	All V_{PS1} pins
2.	All input and output pins	All other input-output pins

1/ Table II is restated in narrative form in 3.4 below.

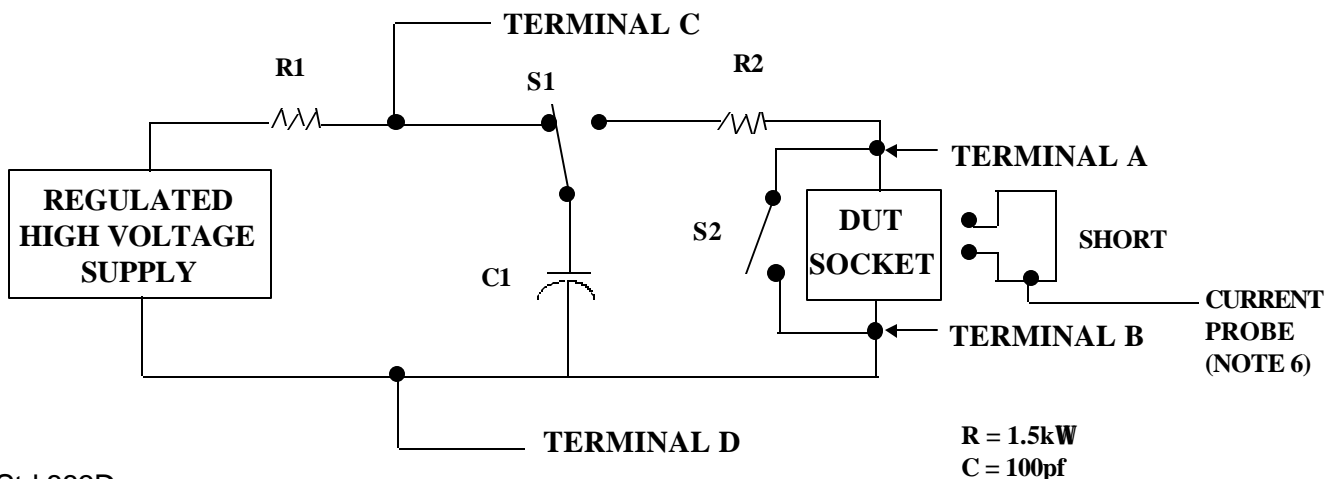
2/ No connects are not to be tested.

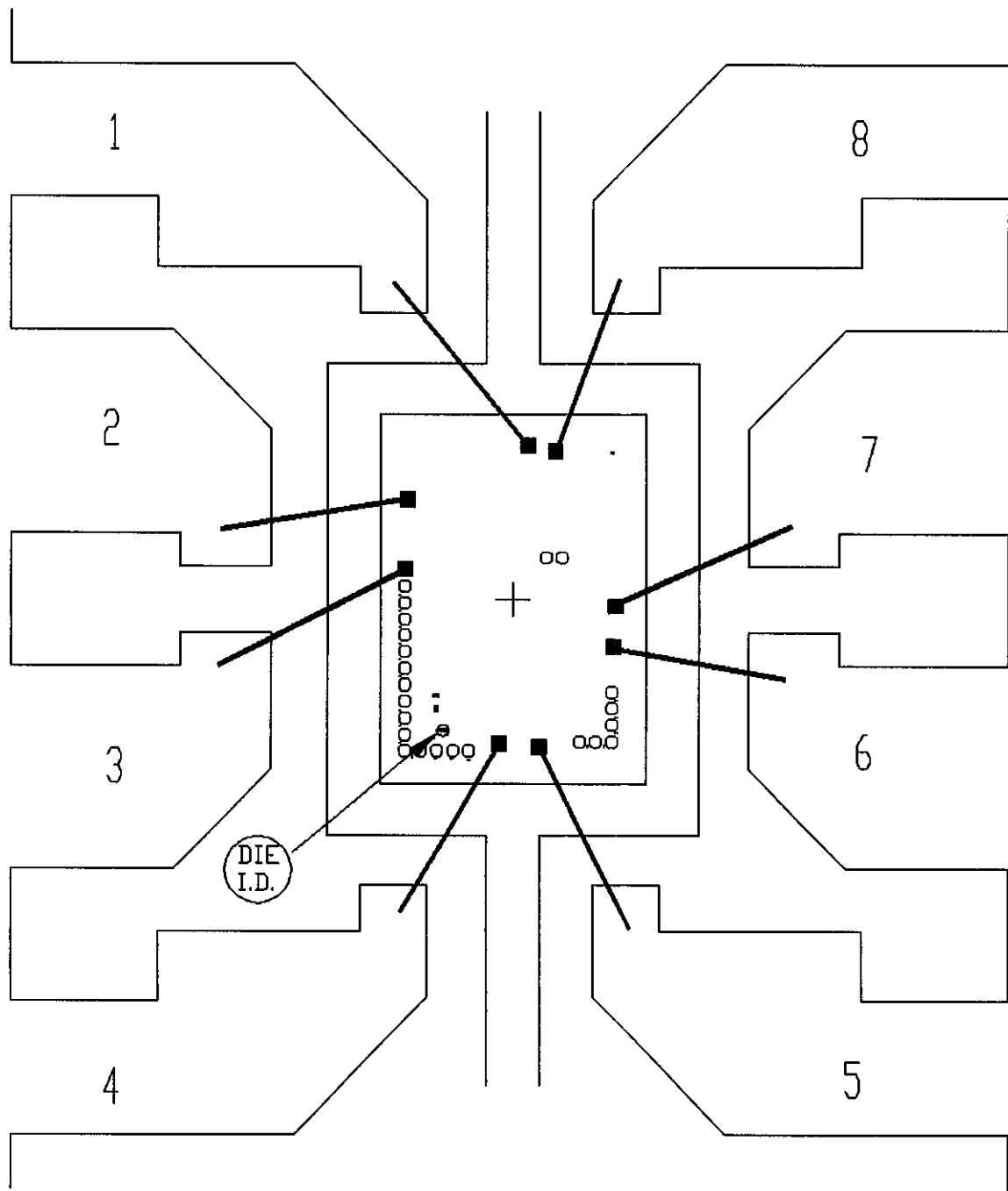
3/ Repeat pin combination I for each named Power supply and for ground

(e.g., where V_{PS1} is V_{DD} , V_{CC} , V_{SS} , V_{BB} , GND, $+V_S$, $-V_S$, V_{REF} , etc).

3.4 Pin combinations to be tested.

- a. Each pin individually connected to terminal A with respect to the device ground pin(s) connected to terminal B. All pins except the one being tested and the ground pin(s) shall be open.
- b. Each pin individually connected to terminal A with respect to each different set of a combination of all named power supply pins (e.g., V_{SS1} , or V_{SS2} or V_{SS3} or V_{CC1} , or V_{CC2}) connected to terminal B. All pins except the one being tested and the power supply pin or set of pins shall be open.
- c. Each input and each output individually connected to terminal A with respect to a combination of all the other input and output pins connected to terminal B. All pins except the input or output pin being tested and the combination of all the other input and output pins shall be open.





PKG.CODE: P8-2

CAV./PAD SIZE: 110 X 140

PKG.
DESIGN

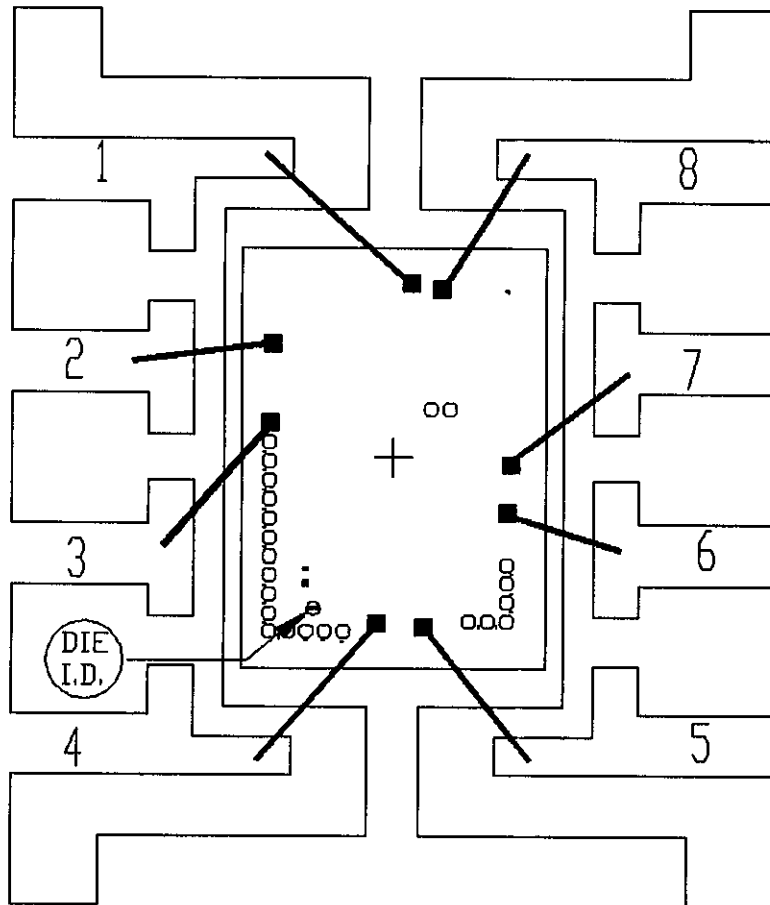
APPROVALS

DATE

MAXIM

BUILDSHEET NUMBER:
05-1701-0240

REV.:
A



PKG.CODE: S8-4

CAV./PAD SIZE: 90 X 130

PKG.
DESIGN

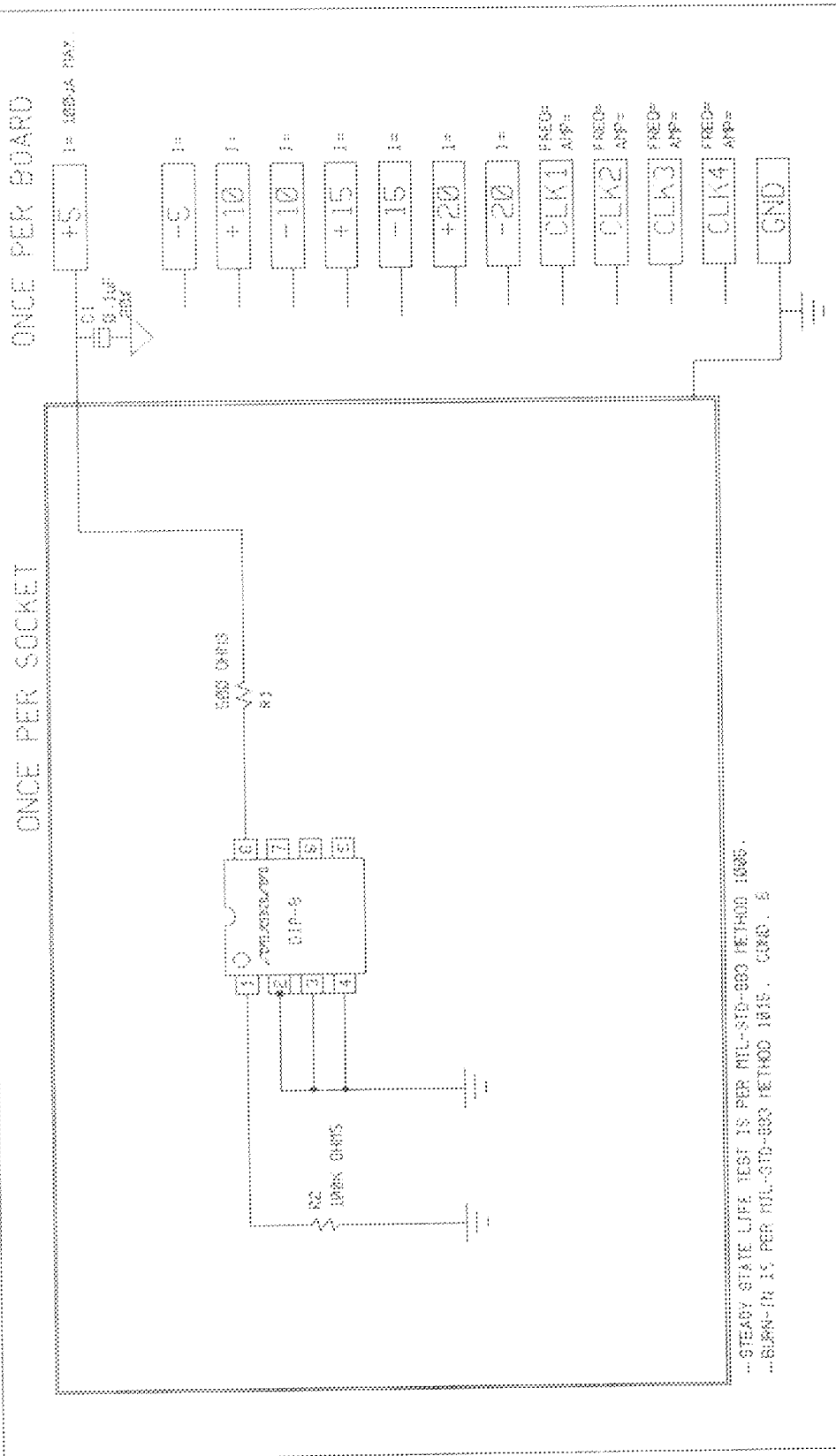
APPROVALS

DATE

MAXIM

BUILDSHEET NUMBER:
05-1701-0241

REV.:
A



ONCE PER BOARD

ONCE PER SOCKET

<p>NOTES:</p> <ol style="list-style-type: none"> TEMPERATURE: 125C OR EQUIVALENT TIME: 168 HOURS MIN. OR EQUIVALENT ALL COMPONENTS AND MATERIAL MUST STAND 155C ENVIRONMENT APPROVED FOR EXI COMMERCIAL EXI HR/83 	<p>SPEC. NO. 06-526 REV. E</p>	<p>MAXIM BURN-IN SCHEMATIC</p>
<p>DATE: 11/24/95</p>	<p>DRAWN BY:</p>	<p>DEVIce TYPE: MAX980/698A/532/452A/534 MAX200/781/793/794/799 MAX984/585/595 MAX 917/818/819</p>