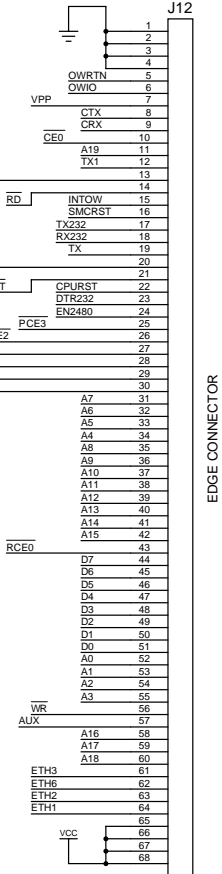
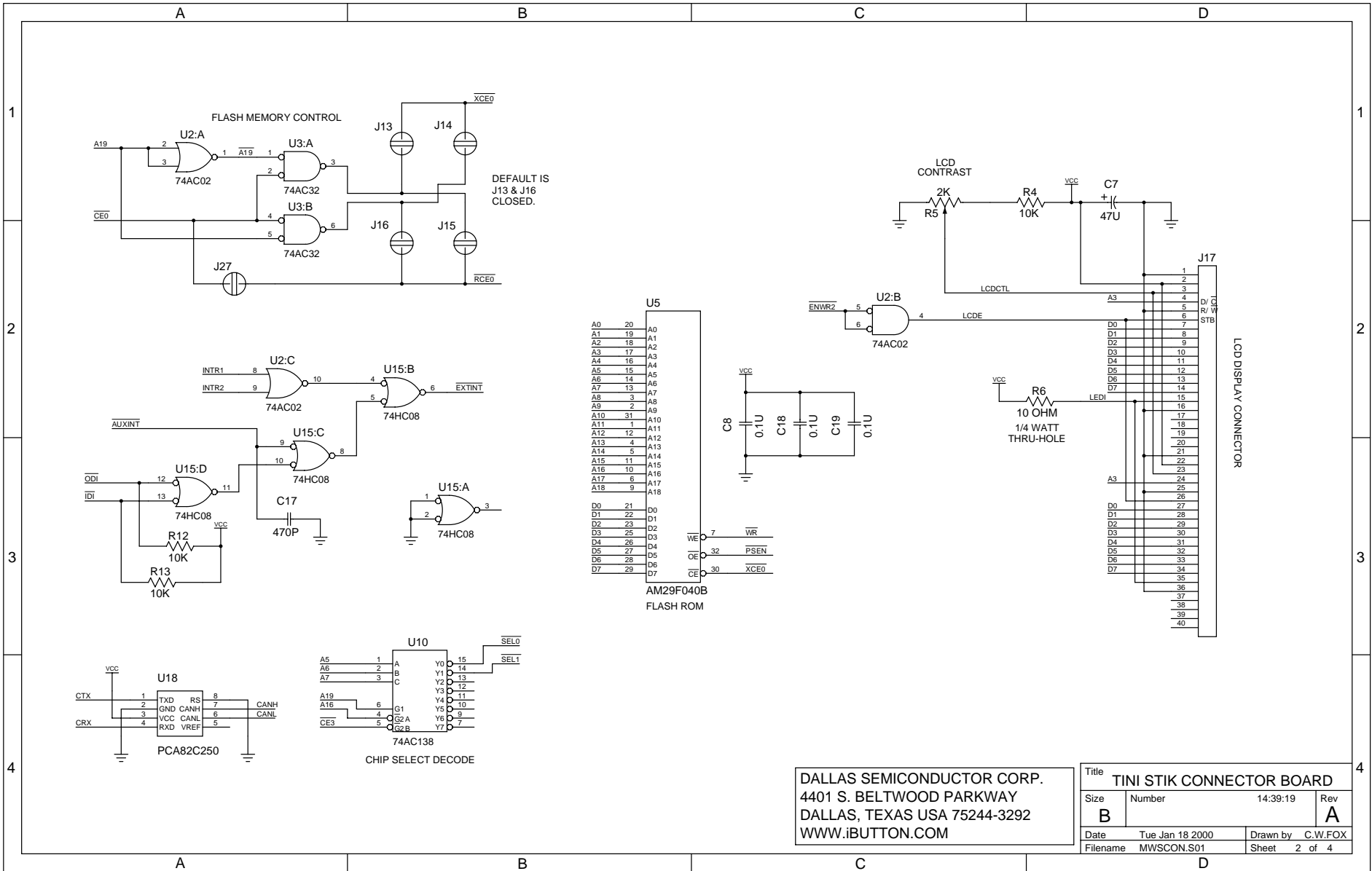


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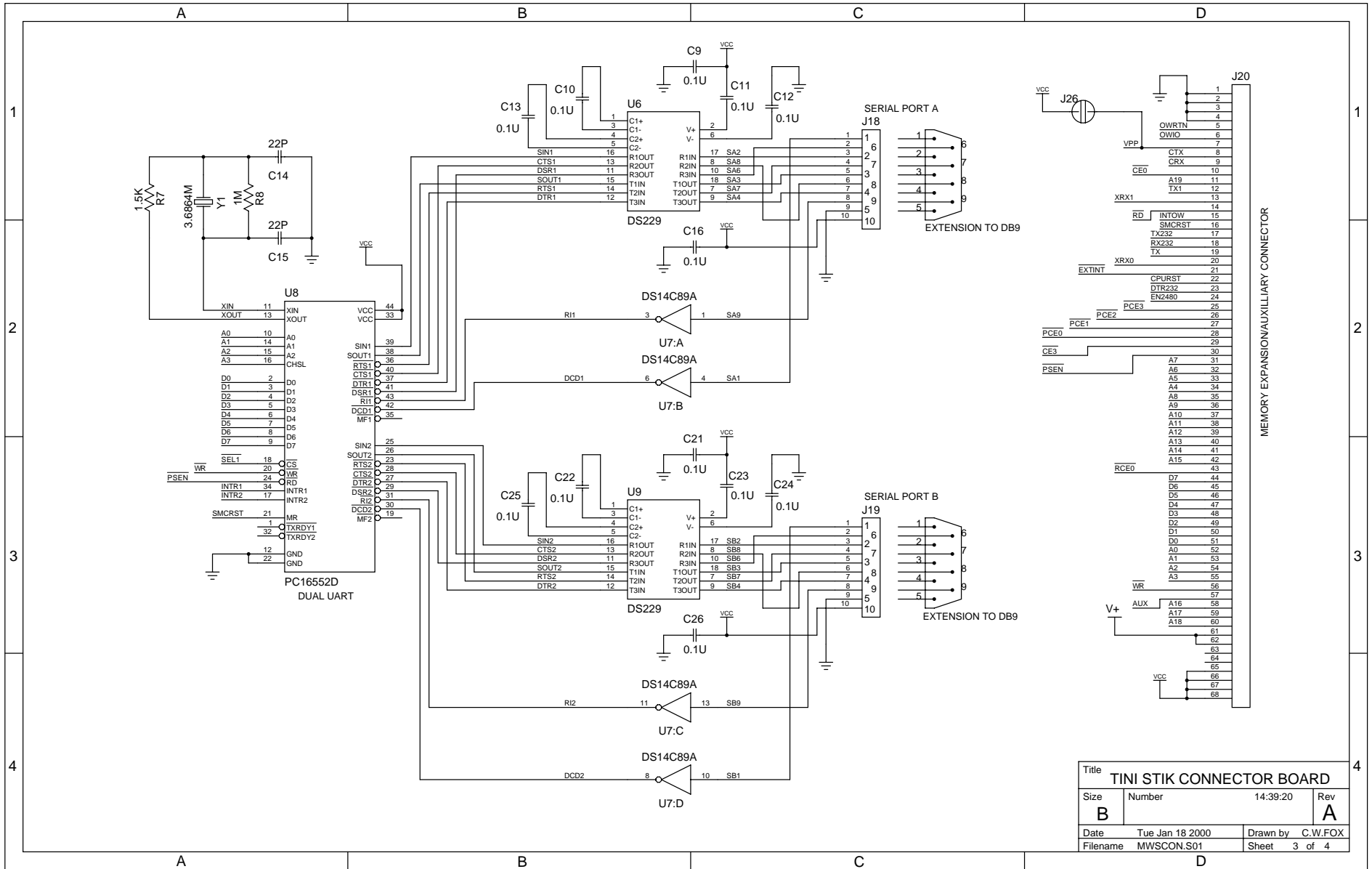
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Size B	Number 14:39:19	Rev A
Date Tue Jan 18 2000	Drawn by C.W.FOX	
Filename MWSCON.S01	Sheet 1 of 4	



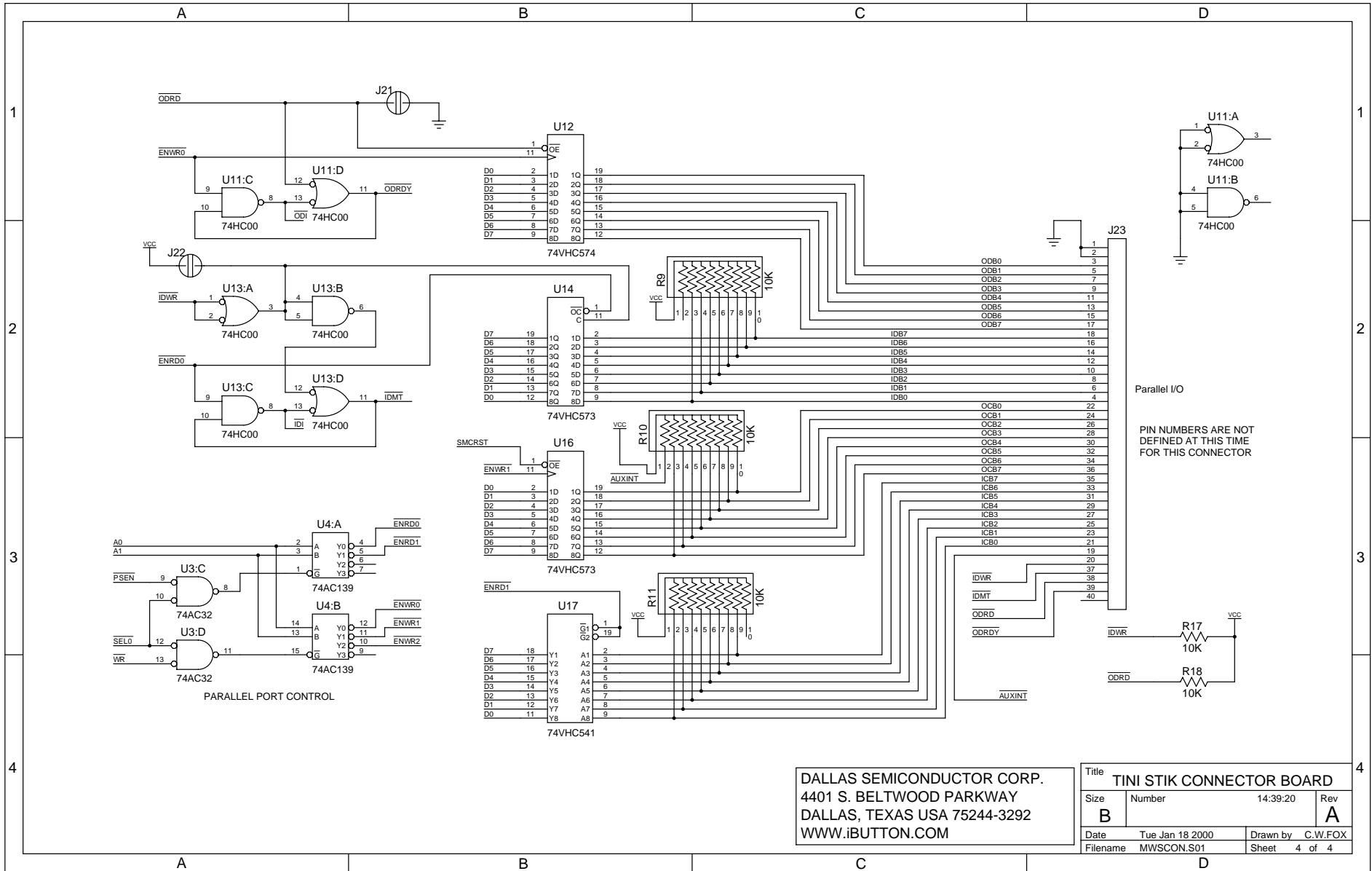


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Title TINI STIK CONNECTOR BOARD		
Size B	Number	14:39:19
Date Tue Jan 18 2000	Drawn by C.W.FOX	Rev A
Filename MWSCON.S01	Sheet 2 of 4	

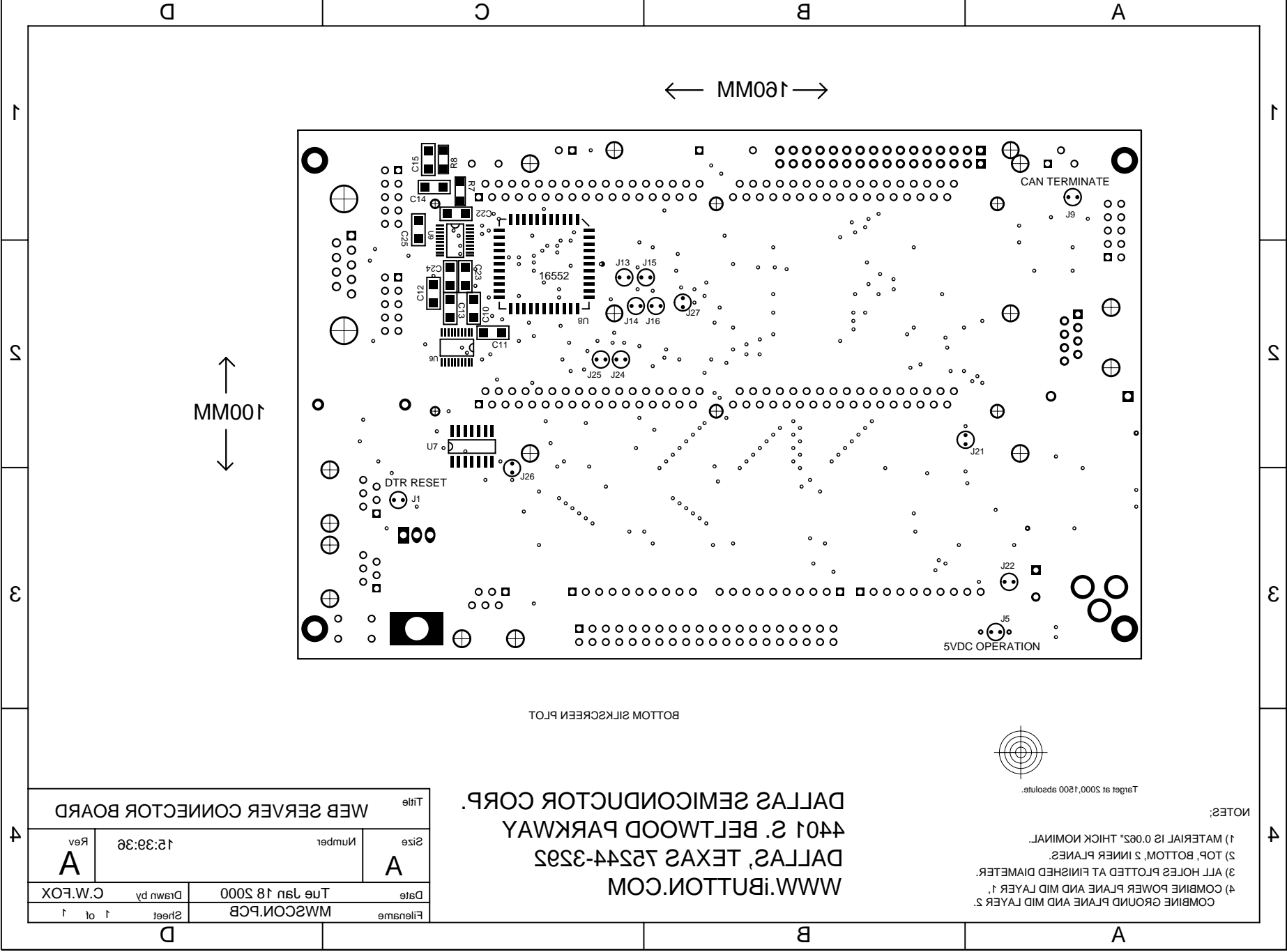


Title			
TINI STIK CONNECTOR BOARD			
Size	Number	14:39:20	Rev
B			A
Date	Tue Jan 18 2000	Drawn by	C.W.FOX
Filename	MWSCON.S01	Sheet	3 of 4



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Title TINI STIK CONNECTOR BOARD			
Size B	Number	14:39:20	Rev A
Date Tue Jan 18 2000	Drawn by C.W.FOX		
Filename MWSCON.S01	Sheet 4 of 4		



Title WEB SERVER CONNECTOR BOARD		Size A	Number 18:38:38	Rev A
File name MWSCON.PCB	Date Tue Jan 18 2000	Drawn by C.W.FOX	Sheet 1 of 1	

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- NOTES:
- 1) MATERIAL IS 0.062" THICK NOMINAL.
 - 2) TOP, BOTTOM, 2 INNER PLANES.
 - 3) ALL HOLES PLOTTED AT FINISHED DIAMETER.
 - 4) COMBINE POWER PLANE AND MID LAYER 1.
 - COMBINE GROUND PLANE AND MID LAYER 2.



BOTTOM SILKSCREEN PLOT

Socket Plus PC Board – Rev A **PRELIMINARY**
(Supersedes a previous preliminary version marked “Rev F”)

Jumper Schedule:

Jumper:	Default:	Function:
J1	Closed	When closed, DTR on Serial Port 0 causes TINI board Reset
J5	Open	When Closed, external power is coupled directly to Vcc bus. <i>CAUTION: This jumper should only be closed if the regulator components are omitted and the external power supply is 5VDC.</i>
J9	Open	When closed, provides 124 ohm termination for the CAN bus.
J24	Open	When closed, allows output control bit OCB7 to control the TO-220 power MOSFET device. <i>Jumpers J24 and J25 should never both be closed at the same time.</i>
J24	Open	When closed, allows CPU port pin CTX to control the TO-220 power MOSFET device. <i>Cannot be used for power MOSFET control when CAN bus is in use. Jumpers J24 and J25 should never both be closed at the same time.</i>
J13	Closed	Closed to select the Socket Plus Flash ROM (U5) in the address range from 080000H through 0FFFFFFH (See note below)
J14	Open	Closed to select the Socket Plus Flash ROM (U5) in the address range from 000000H through 07FFFFFFH (See note below)
J15	Open	Closed to select the TINI On-Stick Flash ROM in the address range from 080000H through 0FFFFFFH (See note below)
J16	Closed	Closed to select the TINI On-Stick Flash ROM in the address range from 000000H through 07FFFFFFH (See note below)
J27	Per Build	Closed when components U2, U3 and U5 are omitted from the Socket Plus board.
J26	Per Build	Closed when the auxilliary 68 pin SIMM module (U20) will not provide power for the Vpp pin.
J21	Per Build	Closed when component U11 is omitted from the Socket Plus board. Sets ODB0-7 to the output (Low-Z) mode.
J22	Per Build	Closed when component U13 is omitted from the Socket Plus board. Sets IDB0-7 to the transparent-input mode.

Notes:

All the solder jumpers are located on the rear (bottom) PC board surface. “Closed” implies that the jumper pads are shorted with solder. “Open” implies that there is no connection between the jumper pads. Jumper settings often depend on the components that are present and/or the functions desired by the application.

J13, J14, J15 and J16 are used to configure the memory mapping of the on-stick and off-stick Flash ROM devices. Each 512K device may be configured to appear in either the boot-up (0000000H – 07FFFFFFH) *CE0 position, or in the upper *CE0 (0800000H – 0FFFFFFH) position. If the logic to support this addressing is not included in the PC Board build, then jumper J27 must be closed to pass *CE0 to *RCE0 and thereby enable the on-stick Flash ROM in the boot-up position. When J14 and J15 are closed and J13 and J16 are open, the TINI stick will boot-up running code from the off-stick Socket Plus Flash ROM device (U5). Devices U2 and U3 must be installed for dynamic switching of the Flash ROM devices to be performed.

-end-